



Docket No.: 2004-0020

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Simon KNOWLES : Confirmation Number: 3818
Application No.: 10/813,615 : Group Art Unit: 2183
Filed: March 31, 2004 : Examiner: Ryan P Fiegle

For: APPARATUS AND METHOD FOR SEPARATE ASYMMETRIC CONTROL
PROCESSING AND DATA PATH PROCESSING IN A DUAL PATH PROCESSOR

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.

In accordance with 37 CFR 1.17(p), please charge the fee of \$180.00 to Deposit Account No. 500417.

10/19/2006 MBERHE 00000045 500417 10013615
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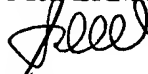
The references **US 2002/010852, US 2002/174266, US 6,061,367, US 2002/063577, and the reference by Alippi, et al.** were first cited in an International Search Report of corresponding US Patent Application No. 10/813,433. The reference **US 5,737,631** was first cited on the form PTO-892 dated May 22, 2006 of corresponding US Patent Application No. 10/813,433.

The references **US 5,600,810, US 2003/0154358 A1, and the reference by BEEBE, et al.** were first cited on the form PTO-1449 dated September 1, 2006 of corresponding US Patent Application No. 10/813,628. The references **US 6,725,357, US 6,880,150, US 5,956,518, and the references by SIMONEN, et al. and TANENBAUM** were first cited on the form PTO-892 dated May 15, 2006 of corresponding US Patent Application No. 10/813,628 .

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Respectfully submitted,

McDERMOTT WILL & EMERY LLP



Stephen A. Becker
Registration No. 26,527

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 SAB:jjz
Facsimile: 202.756.8087
Date: October 18, 2006

**Please recognize our Customer No. 20277
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SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 066365-0020	SERIAL NO. 10/813,615	
				APPLICANT Simon KNOWLES		
				FILING DATE March 31, 2004	GROUP 2183	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US 6,725,357 B1	04-20-2004	COUSIN		
		US 6,880,150 B1	04-12-2005	TAKAYAMA et al.		
		US 5,956,518	09-21-1999	DEHON et al.		
		US 2003/0154358 A1	08-14-2003	SEONG et al.		
		US 5,600,810	02-04-1997	OHKAMI		
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		US 2002/0010852 A1	01-24-2002	ARNOLD et al.		
		US 2002/0174266 A1	11-21-2002	PALEM et al.		
		US 6,061,367	05-09-2000	SIEMERS		
		US 2002/0063577 A1	05-30-2002	ABBOTT		
		US				
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
		SIMONEN, et al., "Variable-Length Instruction Compression for Area Minimization," IEEE, 2003.				
		TANENBAUM, "Structured Computer Organization," 1984, pp. 10-12, Prentice-Hall, Inc., Englewood Cliffs, NJ.				
		BEEBE, et al., "Instruction sequencing control," IBM Technical Disclosure Bulletin, May 1972, pp. 3599-3611, Vol. 14, No. 12.				
		ALIPPI, et al., "Determining the Optimum Extended Instruction-Set Architecture for Application Specific Reconfigurable VLIW CPUs," IEEE, 2001, pp. 50-56.				
EXAMINER			DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.